

IN THE CLAIMS

Claims 1-12 (Canceled).

13 (Currently Amended). A display comprising:
 a liquid crystal over semiconductor substrate;
 a memory array;
 a pixel array including a plurality of electrodes to interact with a liquid crystal material over said pixel array; and
 a refresh circuit coupled to said memory array and said pixel array, said refresh circuit, said memory, and said pixel array formed on the same substrate, said refresh circuit adapted to feed data received from said memory array back to said memory array to refresh said memory array, and said refresh circuit adapted to refresh said pixel array.

Claims 14-15 (Canceled).

16 (Original). The display of claim 13 wherein said memory array is formed of dynamic random access memory.

Claims 17-25 (Canceled).

26 (Previously Presented). A display comprising:
 a liquid crystal over semiconductor substrate;
 a memory array;
 a pixel array including a plurality of electrodes adapted to interact with a liquid crystal material over said pixel array; and
 a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to refresh said memory array and said pixel array, and said memory array and said pixel array are formed on said substrate with said refresh circuit.

Claim 27 (Canceled).

28 (Previously Presented). The display of claim 26 wherein the pixel array is coupled to the memory array by a digital to analog converter.

29 (Previously Presented). The display of claim 26 wherein the pixel array forms a reflective liquid crystal spatial light modulator.

30 (Previously Presented). The display of claim 26 wherein the pixel array is adapted to eliminate a need for a periodic pixel refresh cycle.

31 (Previously Presented). A display comprising:
a memory array formed of dynamic random access memory;
a pixel array forming a reflective crystal spatial light modulator, said array adapted to eliminate a need for a periodic pixel refresh cycle; and
a refresh circuit coupled to said memory array and said pixel array, said refresh circuit adapted to refresh said memory array and said pixel array.

32 (Previously Presented). The display of claim 31 wherein the pixel array is coupled to the memory array by a digital to analog converter.

Claims 33-34 (Canceled).